

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 7,275,189 B2  
APPLICATION NO. : 10/724135  
DATED : September 25, 2007  
INVENTOR(S) : Ruckerbauer et al.

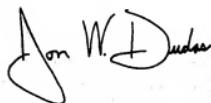
Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In Col. 4, line 31, after "being", insert --used--.  
In Col. 7, line 47, after "12-bit", insert --redundancy data, for a user data bus of 64 bits  
in the case of data memory devices with a data bus width of 8 bits, the  
compensation of defective addressing lines in the data memory devices--.  
In Col. 7, line 48, after "failure of", insert --a--.  
In Col. 11, line 2, delete "has".

Signed and Sealed this

Fourth Day of December, 2007



JON W. DUDAS  
*Director of the United States Patent and Trademark Office*